

ACTIVITY REPORT

World Intellectual Property Day celebration organized by the department of E&C Engineering on 26/4/2014

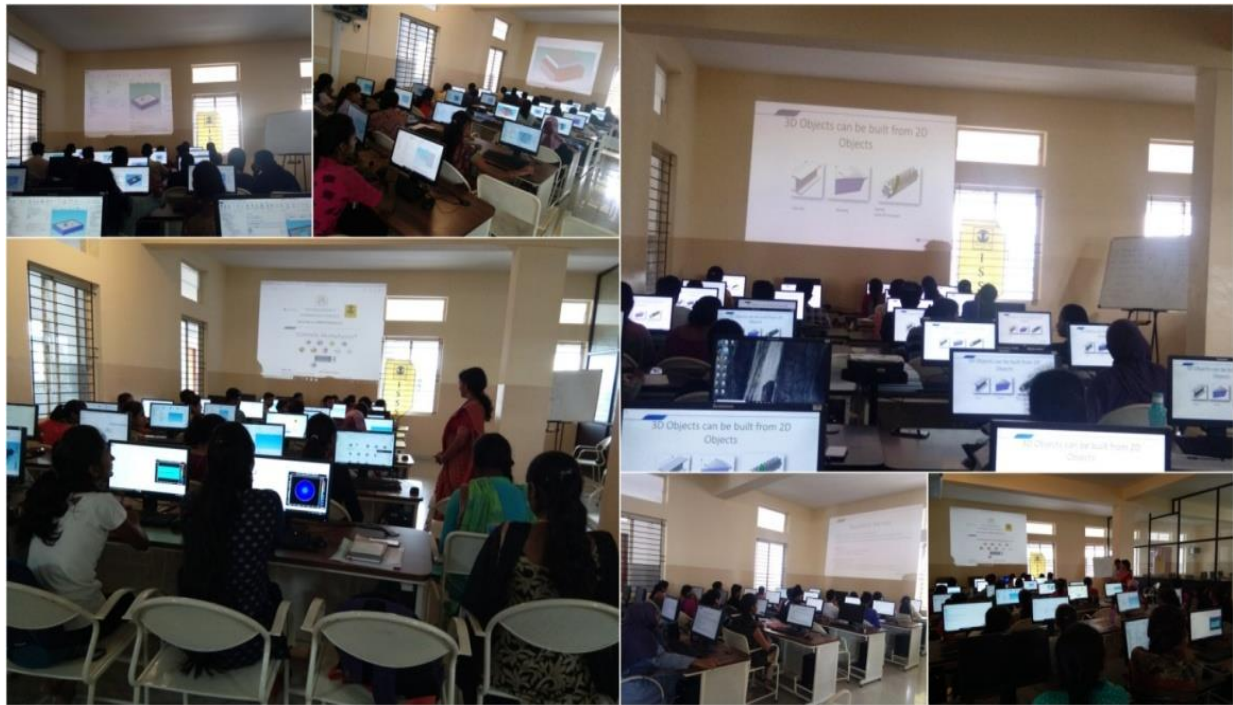


One day Faculty development program on “Protection of Potential Inventions through Patent Filing” by department of E&C Engineering on 27/12/2013, Mr. R C Jagadesh, Vice Chairman, CII, Mysore and Director -Operations & Technology, Klueber Lubrication India Private Limited, Faculties from various VTU engineering colleges have attended this FDP.



Four - Weeks Skill Development Training & Internship on COMSOL Multiphysics

Department of Electronics and Communication Engineering, Vidya Vikas Institute of Engineering and Technology, Mysuru have organized four week Skill Development Training/ Internship on COMSOL Multiphysics in association with **COMSOL multiphysics pvt ltd, USA**. The agenda of this training is to encourage young students to take up designing and simulating the MEMS based devices, three weeks hands on session and one week webinar is organized as a part of the training, during the training participants can be able to design some of the MEMS devices such as accelerometer, piezoelectric pressure sensor, Heat sink, Capacitive pressure sensor, MOSFET etc. the advantage of the training is that participants will going to get participation certificate from **COMSOL** Industry. In the first batch around 32 students have participated out of which 6 students from GMIT, Mandya and the rest are from VVIET, students are felt very happy after the completion of the course. The training is also helping the students to fulfill their four week internship program prescribed by the VTU.



Vidya Vikas Institute of Engineering and Technology (VVIET), Mysuru has taken an initiative to conduct industry standard skill development program on PCB design and PCB Manufacturing in association with Indian Printed Circuit Association (IPCA) & Electronics Sector Skills Council of India (ESSCI).



VIDYA VIKAS INSTITUTE OF ENGINEERING & TECHNOLOGY
DEPARTMENT OF ELECTRICAL & ELECTRONICS ENGINEERING
Brief Report on Workshop “Digital Signal Processing and Its Applications”

Name of the Event	Workshop
Date	24th February 2018
Venue	First Floor Seminar Hall
Department	Department of EEE
No. of Students / Faculties Participated	100 students
Name of the Speaker(s)/Guest(s) if any	Dr. Basavaraju S Project Manager L&T, Bengaluru Dr. Jagadeesh B Kanade Project Lead, EM- TARANGA Software, Bengaluru
Outcome of the Program	Students were able to understand Digital Signal Processors, Practical illustration by examples in the areas of 1-D and 2-D linear signal processing. Exposures to filtering effects in signal processing, Properties of DSP and Applications of DSP.
Programme Co-ordinators	Prof. Shamala N, Prof. Manjunatha B, Prof Varsha V and Prof. Sushma H S
Graduate attribute	Engineering Knowledge , The Engineer & Society

Brief Report about the Program:

Vidya Vikas Institute of Engineering & Technology (VVIET) - Department of Electrical & Electronics Engineering, conducted Workshop on “Digital Signal Processing and Its Applications”. The event was conducted between 9:30PM to 4:30PM. Around 100 students of 6thSem(‘A’ & ‘B’ section) participated in this Event.

The resource person invited was Dr. Basavaraju S Project Manager L&T, Bengaluru and Dr. Jagadeesh B Kanade Project Lead, EM- TARANGA Software, Bengaluru.

The participants of this workshop were exposed to the utility of the MATLAB-DSP education software in multidisciplinary signal and data analysis applications. The session was interactive and participants used exercise that involves step-by-step simulations using

MATLAB-DSP. Apart from basic modules that will be useful for DSP courses, new modules in music and audio signal processing, wireless communications, and power systems was demonstrated. The workshop was useful to instructors and practitioners with interests in signals and systems, DSP and other applications.



Dr. Basavaraju S explaining about DSP Concept



Dr. Jagadeesh B Kanade explaining about MATLAB



Students hands on session using MATLAB software



Group Photo of the event with Resource Person, Dr Ravishankar M. Principal, Dr. Surekha Manoj, HOD of EEE Dept, Event Co-ordinators-Prof. Shamala N, Prof. Manjunatha B, Prof Varsha V and Prof. Sushma H S, Faculty of EEE Dept and Participants.

Brief Report on Workshop on PCB Technology & Soldering Practices

Name of the Event	Workshop on PCB Technology & Soldering Practices
Date	02/04/2016
Venue	Logic Design Lab
Department	Department of EEE
No. of Students / Faculties Participated	71 students of 4th sem
Name of the Speaker(s)/Guest(s) if any	Mr. Mohammed Sadhiq Ali & Mr. Surender Kumar S
Outcome of the Programme	Learning the fundamentals of Printed circuit boards and their importance, in Electronics industry and soldering practice, which are not available in the curriculum and will be a motivation for the students to learn more in technology apart from syllabus and do projects . It will also be useful for placement opportunities
Programme Co-ordinators	Faculties: Sri Hemanth Kumar S / Smt Swathi K
Graduate attribute	Engineering knowledge, The engineer & society, Individual & team work

Brief Report about the Program:

Department of Electrical & Electronics Engineering had organized a Workshop on PCB Technology & Soldering Practices which they had done as co-curricular activity for the 4th semester students on 02/04/16, which was a successful event. Students showed positive and enthusiastic response towards this event. The students actively participated without any hesitation and learnt about new trends in PCB Technology and practiced the Soldering Techniques. This was a platform for the students to improve their skills like circuit assembling, soldering, testing and troubleshooting.

Welcoming the Chief Guest



Technical Session





Practical Session





Honoring the Guest



A Brief Report on PLC workshop

Name of the Event	PLC Workshop
Date	16/11/17
Venue	PSS Lab & Microcontroller Lab
Department	Department of EEE
No. of Students / Faculties Participated	89 students from 5 th and 7 th sem
Names of the Trainers	C.S.Kalaikovan, Arif, Gajendra and Pradeep Kumar from Prolific Systems Pvt. Ltd
Outcome of the Program.	Introduction to PLC and its application in Automation Industries. Hardware Interfacing with Delta PLC. Build up and Execution of different PLC programs Using Delta PLC simulation Software
Programme Co-ordinators	Faculties: Sri. Yathish Babu A.M
Graduate attribute	Engineering Knowledge, Individual & team work, Communication

Brief Report about the Program:

Department of Electrical & Electronics Engineering had organized a one day workshop on PLC for the 5th and 7th semester students on 16/11/2017, which was a successful event. Students showed positive and enthusiastic response towards this event. The students actively participated without any hesitation and gained knowledge on Hardware Interfacing with Delta PLC, Build up and Execution of different PLC programs using Delta PLC simulation Software. As they had an elective on PLC, this was a platform for the students to improve their skills to use PLC hardware.

Photos:



Student CEO -2015 Competition

Agenda / Subject: To give students the opportunities to expose their leadership qualities.

Guests: Dr.Ravishankar ,Principal ,VVIET,Mysore

Dr.Krishana Athre,Director ,VVIET,Mysore

Prof. Renukha Murthy, Director & HOD of MBA dept,VTU,Mysore

Abhishek I Alumni ,IS&E dept,VVIET,Mysore

Outcome of Programme: students are better equipped to handle challenges in their employment

Faculty Coordinators:K.B.Drakshayini

Omar Khan Durani

Date:29/8/2015



Winner of Student CEO 2015 competition from ISE



Winner of Student CEO 2015 competition from CSE



Evaluation process





“Yi Leadership Academy (LA) for Yuva Faculty Members”

The objective of the program was to enhance the leadership skills of the faculty, develop an understanding of Yuva, and to motivate them to promote leadership development for our students. They also aimed to create a platform for faculties to understand latest trends in education sector.

Date: 25-26 August 2017

Venue: Indian School of Business, Hyderabad

Registration Fees : 1500 per person + tax

Num of participants from VVIET : 3





more photos at:

<https://goo.gl/photos/EndTFtoqBjq3kktB7>

"Student Registration for YI-YUVA"

Date: 20/09/17

Venue: VVIET, Mysuru

Time: 10:00pm to 12:00

Registration Fees : 100 per person

Num of students registered for Yi-YUVA : 363





"Rally for Rivers"

Rally for Rivers is a nationwide awareness campaign to save the Indian rivers.

Date: 08/09/17

Venue: Maharaja Grounds

Time: 6:00pm to 9:00 pm

Entry Fees : -NIL-

Num of participants from VVIET : 30



“ Uncovering the management lessons of the Bhagavad Gita”
- Workshop by Dr. Pai

Date: 11th October 2017

Venue : Amrita Vishwa Vidyapeetham , Bogadi, Mysuru.

Time : 10 am

Num of participants from VVIET : 15

Yi
Young Indians
WISDOM. LIVE WISE

CII
Confederation of Indian Industry

UVA
Presents

Uncovering the management
ಲೆಡ್ಡನ್ಸ್ of the
bhagavad gita

Management principles from Bhagavad Gita
Workshop by Dr. Pai

Place:
Amrita Vishwa Vidyapeetham,
Bogadi, Mysuru.

Date:
11th October 2017
Time : 10 am

Open event for Registered Yuva Students & Yians only



"Global Youth Initiative- leadership training"

The event was dedicated to helping the teens become bright leaders and change makers.

Date: 13/10/17

Venue: VVCE Auditorium

Time: 3:00pm to 6:30 pm

Entry Fees : -NIL-

Num of participants from VVIET : 70

Yi
Young Indians
WE CAN WE WILL

UVA
Learning Beyond Education

RENERGETICS CONSULTING
Unlock Your Potential
www.renergetics.com

The JOHN MAXWELL Team
presents
GLOBAL YOUTH INITIATIVE WEEK
OCTOBER 9-15, 2017
A full week dedicated to helping kids & teens become bright leaders & change makers!

During the event, youth will be equipped, inspired and empowered to:

- Have a Positive Self Image**
Self-confidence and good character are at the heart of great leaders.
- Respect Themselves and Others**
Great leaders do not bully or put others down. They value people and build them up.
- Be a Positive Influence**
Leadership is influence. Young people are the leaders of today!
- Fail Forward Toward Success**
Leaders have the right attitude about failure as an important part of success.

UVA is glad to announce its partnership with 'The John Maxwell Team' to promote **Global Youth Initiative (GYI)** in India between **Oct 9th – 15th 2017**. A full week dedicated to our YUVA students become the **Bright Leaders and Change Makers of tomorrow!**

Please send us your **Expression of Interest (EOI)** at shikhar.kant@cii.in to conduct this training session (2-4 hours) at your YUVA Chapter by **22 September 2017**.



More Photos at :

https://drive.google.com/open?id=0B_9OxUmxyPTZS1NhbtJNV1VtM2M
<https://drive.google.com/drive/folders/0B9duiU1BZi2ReFBBdV9xRk9hckE>

"Young India - New India"

The event highlighted the role of youth in nation building.

Date: 09/11/17

Venue: VVIET First Floor Seminar Hall

Time: 10:00am to 1:00 pm

Entry Fees : -NIL-

Num of participants from VVIET : 120



The poster features a yellow and green background with a central white wheel. At the top, there are three logos: 'Yi Young Indians WE CAN WE WILL', 'YUVA', and 'CII Confederation of Indian Industry'. Below the logos is a circular portrait of Sivakumar Palaniappan. The main text reads: 'Sivakumar Palaniappan's Talk Series on Role of Youth in Nation Building "Young India - New India"'. At the bottom, it states: '09 November 2017, 10.00 am Vidya Vikas Institute of Technology, Mysuru'.

Yi
Young Indians
WE CAN WE WILL

YUVA

CII
Confederation of Indian Industry

Sivakumar Palaniappan's
Talk Series on Role of Youth in Nation Building
"Young India - New India"

09 November 2017, 10.00 am
Vidya Vikas Institute of Technology, Mysuru



Certificate Course in PCB Design and Manufacturing

Vidya Vikas Institute of Engineering and Technology has taken an initiative to conduct industry standard skill development program on PCB design and PCB Manufacturing in association with Indian Printed Circuit Association (IPCA) & Electronics Sector Skills Council of India (ESSCI). The Institute became a member of Indian Printed Circuit Association on 31.08.2018.

The Vidya Vikas Institute of Engineering and Technology, Mysuru, and Indian Printed Circuit Association, Bengaluru signed a Memorandum of understanding followed by the PCB Design and Manufacturing Training.

The Indian Printed Circuit Association (IPCA) is a voluntary, non-profit, service-oriented professional organization comprising leading technical experts of PCB manufacturing in the country. The Association's primary objective is to promote learning and the advance education in the science and application of electronic packaging, namely Printed Circuit and inter-connection techniques. Indian Printed circuit Association (IPCA) is one of the promoter of Electronic Sector Skills council of India. IPCA is knowledge partner for ESSCI for skill development in Design and Manufacturing of Printed Circuit Board(PCB)

Electronics Sector Skills Council of India (ESSCI) is a Not-for-Profit Organization, registered under the Indian Companies Act, 1956. The Council has been promoted by six Associations i.e. CEAMA, ELCINA, IESA (formerly ISA) , IPCA, MAIT & ELCOMA, with financial support by National Skill Development Corporation (NSDC). Electronics Sector Skills Council of India envisions to enable a world class electronics manufacturing industry with an ecosystem for skill development and enhance employability of the large number of Indian human resource.

At present Indian PCB Industry facing the following Challenges.

1. Capital Intensive manufacturing Set up
2. Inadequate supply chain for raw materials
3. Lack of improved technology and lack of technology management skill.
4. Unfair playing field : High finance cost, Lower subsidies, poor ecosystem
5. Logistic inefficiency & infrastructural bottleneck
6. Lack of Skilled Manpower

The lack of skilled manpower is due to the following

1. No institution imparts fabrication oriented training.
2. Industry does not encourage summer training for students from technical schools or colleges.
3. Industry faces challenges in attracting talents.
4. There is no structured training program in industry for skill development of existing work force and newly inducted employees except few industries.
5. Many industries do not use the quality tools to analyze the issues and come to a right conclusion.

IPCA has established interaction with six Institutions across India as its training centers for introducing 6 months certificate course at Level-5 for PCB Design & Manufacturing. In BIT , Sathyamangalam(TN) & VVIET , Mysore process of training faculties has been started. ESSCI has declared Six Center of Excellence(CoE) for PCB Design & manufacturing in its last board meeting held on 30th June, 2017. IPCA is in process of interacting with industry members & faculty from institutions to create a pool of trainers with experience in core knowledge. ESSCI will support these professionals in platform training . ESSCI has already formed NOS committee for PCB designing & manufacturing.

Course Title : Certificate Course in PCB Manufacturing

Job Role : PCB Manufacturing Professional (Level-5)

Objectives :

1. Acquire knowledge of different types of PCB and Materials used in manufacturing of PCBs
2. Understanding of complete PCB manufacturing process steps of
 - a. Single Sided(SS) PCBs
 - b. Double Sided(DS)PCBs
 - c. Multilayer(ML) PCBs
3. Understanding Process Parameters and Process Control activities
4. Awareness of Acceptability Standard of PCBs and Customer Specifications.
5. Material handling and handling of products in entire chain of Manufacturing

Course Title : Certificate Course in PCB Design

Job Role : PCB Design Professional (Level-5)

Objectives :

1. Acquire knowledge of different types of PCB and Electronic components package types
2. Understanding of complete PCB design steps
 - a. Create schematics diagram
 - b. Create PCB layout
 - c. Converting them to Gerber format with detailed instructions and information, using CAD and other software tools

Vidya Vikas Institute of Engineering & Technology organized the following events in association with IPCA and ESSCI.

1. Three days Faculty Training Program on "PCB Design" from 09-12-2017 to 11-12-2017. The resource person is Mr.Vinay S, Manager, Nuline Technologies Pvt Ltd, Bengaluru .
2. Seven days Faculty Training Program on "PCB Manufacturing" from 11-01-18 to 23-01-18. The resource persons for the event are Mr. Amulya Mohapatra (AT&S), Mr Biju Thomas(Atotech India Pvt. Ltd.), Mr. Rammohan G(Atotech India Pvt. Ltd.), Mr Ramni V(KBL Laminates and Technology), Mr Harish M A (AT&S), Dr Shivakumar (AT & S) and Mr. Nagaraj, IPC certifide trainer.
3. Four days Plat form training (soft skill) from 09-03-2018 to 12-03-2018. The resource person is Mr.Palani,Master Trainer ,ESSCI,New Dehli.

4. Sent two faculties for two days workshop on Multilayer PCB manufacturing process organized by IPCA at Atotech, Bengaluru.
5. Sent two faculties for three days “Master trainer training program” organized by ESSCI in BIT,Sathyamangalam,Tamilnadu from 16-11-2018 to 18-11-2018.
6. Arranged industry visit to AT & S for one day orientation and three days individual group visit .

List of Faculties who Undergone PCB Design Training

Sl No.	Faculty Name	Department	Designation
1	Mrs. Anitha D B	ECE	Associate Professor
2	Mr. Nitesh K A		Assistant Professor
3	Ms. Asha A		Assistant Professor
4	Mr. Surendar	EEE	Assistant Professor
5	Mr. Manjunatha		Assistant Professor
6	Mr.Allwin Yesuvadian	ME	Assistant Professor
7	Mrs. Ramya	CSE	Assistant Professor
8	Ms. Rakshitha		Assistant Professor

List of Faculties who Undergone PCB Manufacturing Training

Sl No.	Faculty Name	Department	Designation
1	Mrs. Anitha D B	ECE	Associate Professor
2	Mr. Nitesh K A		Assistant Professor
3	Ms. Asha A		Assistant Professor
4	Mrs. Navyashree D		Assistant Professor

5	Mr. Madhusudhan		Assistant Professor
6	Mr. Surendar	EEE	Assistant Professor
7	Mr. Chikkadevegowda	ME	Associate Professor
8	Mr. Allwin Yesuvadian		Assistant Professor
9	Mr. Sandeep		Assistant Professor
10	Dr. Chamaraju	Chemistry	Assistant Professor
11	Dr. Mujida Banu	Chemistry	Associate Professor
12	Dr.Chidan Kumar	Chemistry	Associate Professor

The faculties from VVIET attended a two day workshop on Multilayer PCB manufacturing process organized by IPCA in Atotech, Bengaluru on October 4th and 5th of 2017.



The faculties of VVIET were taken to the PCB manufacturing company AT&S, Nanjangudu for the orientation of Skill Development Training Program in which the faculties from Department of Electronics and Communication, Department of Electrical and Electronics, Department of Mechanical engineering, Department of Chemistry and Department of Computer Science and Information Science engineering took part actively.



The faculties of VVIET Mrs Anitha D B, Associate Professor, Department of ECE,VVIET and Mr Surendra Kumar, Assistant Professor, Department of EEE had attended the Master Trainers Program organized by ESSCI in BIT, Sathyamangalam, Tamilnadu.



The PCB Design training was a three day program which was held from 09.12.2017 to 11.12.2017. The resource person for PCB design training was Mr Vinay, Manager, Nuline Technologies, Bengaluru. Mr Harish , AT&S had also participated in the PCB design training. Content of the training includes components overview, standard terminology, PCB defects, Circuit design ,schematic diagram creation and simulation, PCB layout design, library creation and Gerber data generation.



The PCB manufacturing training was held from 11.01.2018 to 30.01.2018. Mr Amulya Mahopatra, HoD, Production, AT&S, Member of IPCA has initiated the skill development program in Vidya Vikas Institute of engineering and Technology, Mysuru.

Date	Name of the Industry Person Name	Designation and Company	Duration	Content
11/01/2018	Mr. Amulya Mohapatra	HoD , Production, AT & S,Mysuru	9.30 AM to 5.00 PM	Frontend Process
16/01/2018	Mr. Amulya Mohapatra	HoD , Production, AT & S, Mysuru	1.30 AM to 5.45 PM	Multilayer PCB manufacturing Process
18/01/2018	Mr. Amulya Mohapatra	HoD , Production, AT & S, Mysuru	9.30 AM to 01.15 PM	Drilling
18/01/2018	Mr.Biju Thomas	General Manager,Atotech India Pvt. Ltd. Bengaluru	1.45PM to 5.45 PM	Chemical Process for Plating
19/01/2018	Dr.Shiva Kumar	HoD, Photo printing, AT & S,Mysuru	9.00 AM to 1.15 PM	Photo Printing
19/01/2018	Mr. Rammohan G	Dy.General Manager - SF , Electronics, Atotech India Pvt. Ltd, Bengaluru	1.45 PM to 5.45 PM	Chemical Process for Surface finishing
23/01/2018	Mr. Harish M A	HoD, Backend Process, AT & S,Mysuru	9.00 AM to 1.00 PM	Backend Process- Testing, Final Inspection
30/01/2018	Mr. Amulya Mohapatra	HoD , Production, AT & S, Mysuru	9.30 AM to 01.15 PM	Solder Masking
15/02/2018	Mr. Ramani	Proprieter, KBL Laminates & Technology, Bengaluru	9.00 AM to 1.00 PM	Base materials and Acceptance standards
15/02/2018	Mr. Nagaraj	IPC Certified Trainer	1.45 PM to 5.45 PM	IPC Standards

On day 1 Mr. Amulya explained about Electronic system design and manufacturing (ESDM) Indian PCB industry, IPCA-ESSCI initiatives on skill development, ESSCI Qualification Pack, Introduction to PCB and PCB Technology, Base Materials for PCB manufacturing and Front End Engineering.



On day2 Mr. Amulya explained in detail about the Multilayer PCB manufacturing process, Relamination process. He also mentioned about the Scratch proof PCB, clean room, particle counter, lay-up stations, plates, Prepeg storage process, Kraft paper importance, Stainless steel plates. He discussed about the importance of Hot and Cold Oven and its advantages, In the Hot or Cold oven process Work page, Dent size, Dryness, Core to core shift, press laminate, lot integrity were mentioned along with Do's and Don't's.



On day 3 Mr. AmulyaMohapatra from AT&S, Mysuru started his session with Video presentation on front end tool data preparations. The front end tool data preparations involves Drilling process. He also discussed Pre-drilling process, pre-baking, stack-up preparation, Inner layer layup and multilayer board lamination, Mechanical drilling.

Mr.Biju Thomas General Manager, Atotech India Pvt. Ltd. Bengaluru explained about the different techniques used for PTH process such as Electro less copper plating, direct plating, defects in plating such as interconnect defects, voids, (glass fibre voids, wedge voids, filled wedge void), delamination, negative etch back, positive etch back, pocket void, Foil cracks, pink ring and different testing techniques .



On day 4 Dr. Shiva Kumar HoD, Photo printing, AT&S, Mysuru Explained about precleaning process and photo printing process. Mr. Rammohan G , General Manager, Atotech India Pvt. Ltd, Bengaluru explained about the different types of surface finishing.



On day 5 Mr. Harish M A HoD, Backend Process, AT & S discussed about the PCB Manufacturing process, Routing process, legend printing, Electronic test(BBT test) , final inspection and Packing.



On day 6 Mr. Amulya explained about the solder mask process which involves Curtain coating ,Expose, solder mask final curing. He also explained about the types of printing process- legend, plugging, carbon printing and IRC/Black mask, Peelable Solder mask.



On day 7 Mr. Ramani , Proprietor, KBL Laminates, Bengaluru explained about the acceptance criteria based on IPC standards. Mr. Nagaraj, Certified IPC trainer explained about the IPC and industry standards available for both processes and test methods. He also mentioned about the PCB evaluation which can be done both internal and external.



Faculties undergone PCB manufacturing training is divided in to groups for industry visit to AT & S as follows.

Group	Areas	Name of faculties	Institute	Stream	Dates
A	Product Engineering, AOI and Relamination	Mr. Madhusudhan	VVIET	ECE	10.02.2018
		Mrs. Anitha D B	VVIET	ECE	10.02.2018
		Ms. Asha A	VVIET	ECE	10.02.2018
		Mr. Surender Kumar	VVIET	EEE	10.02.2018
B	X Ray, Drilling, Scoring and Routing	Mr. Chikka devegowda	VVIET	ME	10.02.2018
		Mr. Sandeep B	VVIET	ME	10.02.2018
		Mr. Allwin Yesuvadian	VVIET	ME	10.02.2018
C	Image & Solder Mask	Mrs. Anitha D B	VVIET	ECE	17.02.2018
		Mrs. Navyashree D	VVIET	ECE	17.02.2018
		Mr. Madhusudhan	VVIET	ECE	17.02.2018
		Mr. Nitesh K A	VVIET	ECE	17.02.2018
D	Chemical Processes, EHS	Dr. Chamaraja	VVIET	Chemistry	17.02.2018
		Dr. Mujeeda Banu	VVIET	Chemistry	17.02.2018
		Dr. Chidan Kumar	VVIET	Chemistry	17.02.2018
E	Surface Finishes and Screen Printing	Dr. Chamaraja	VVIET	Chemistry	24.02.2018
		Mr. Surender Kumar	VVIET	EEE	24.02.2018
		Dr. Mujeeda Banu	VVIET	Chemistry	24.02.2018
		Dr. Chidan Kumar	VVIET	Chemistry	24.02.2018
F	Testing, FI, Packing, Specifications & Standards	Ms. Asha A	VVIET	ECE	24.02.2018
		Mrs. Anitha D B	VVIET	ECE	24.02.2018
		Mr. Nitesh K A	VVIET	ECE	24.02.2018
		Mrs. Navyashree D	VVIET	ECE	24.02.2018

Group A and Group B visited AT & S on 17-02-2018 to gain indepth knowledge on Product Engineering ,AOI and Relamination(Group A) and X-Ray,Drilling,Scoring and Routing process(Group B)



Group C and D visited AT & S on 24-02-2018 to gain indepth knowledge on Imaging and Solder Mask (Group C) and Chemical Process and EHS(Group D).



Group E and F visited AT & S on 03-03-2018 to gain indepth knowledge on Surface finish,screen printing,Testing, final inspection , packing, specification abd standards.



Vidya Vikas Institute of Engineering and Technology organized a National level seminar on "Emerging Trends in PCB Design, Manufacturing and Assembly Technology" in association with Indian Printed Circuit Association (IPCA) & Electronics Sector Skills Council of India (ESSCI) on 19th Feb, 2018. This was the first major National Seminar on Emerging Trends in PCB Technology in south India with the objective of Creating awareness about the Emerging trend in Printed Circuit Board Technology and exposing the participants to the scope and challenges in PCB Designing.

The National seminar on Emerging Trends in PCB Technology offers to impart the knowledge of multidisciplinary Engineering engagement in PCB Manufacturing and Emerging trends in PCB assembly Technology. Engineering Students, Engineers and Researchers to provide an opportunity in bringing the new Technology and perspectives that will contribute to Electrical and Electronics Engineering, Electronics and Communication Engineering, in future years. Students and faculty members from various Engineering colleges were participating in this Seminar.

Chief Guests for the National seminar :

- 1) Mr. M Thyagrajan, Treasurer IPCA and CEO, Nuline Technologies, Bengaluru
- 2) Mr. Amulya Mohapatra, Member Technical-IPCA, Bengaluru,
- 3) Dr. Devarj Singh, General Manager-Technical, ESSCI, New Delhi.
- 4) Mr. O J Sathish, CEO, Sienna ECAD, Bengaluru,
- 5) Mr. Ramesh Agrahar, VP, Kaynes Technologies, Mysuru.

Sri Kaveesh V gowda , Secretary , VVIET, Mysuru exchanged Memorandum of Understanding with Mr. M Thyagrajan ,Treasurer, IPCA, Bengaluru.





Four - Weeks Skill Development Training /Internship on COMSOL Multiphysics

Department of Electronics and Communication Engineering, Vidya Vikas Institute of Engineering and Technology, Mysuru have organized four week Skill Development Training/ Internship on COMSOL Multiphysics in association with **COMSOL multiphysics pvt ltd, USA**. The agenda of this training is to encourage young students to take up designing and simulating the MEMS based devices, three weeks hands on session and one week webinar is organized as a part of the training, during the training participants can be able to design some of the MEMS devices such as accelerometer, piezoelectric pressure sensor, Heat sink, Capacitive pressure sensor, MOSFET etc. the advantage of the training is that participants will going to get participation certificate from **COMSOL** Industry. In the first batch around 32 students have participated out of which 6 students from GMIT, Mandya and the rest are from VVIET, students are felt very happy after the completion of the course. The training is also helping the students to fulfill their four week internship program prescribed by the VTU.

